

January 1999

Figures 24, 25, 26 and 44 in **Application Note 70** have pin numbering errors. The revised versions of these Figures are shown below with the correct pin number in oversized and bold type.

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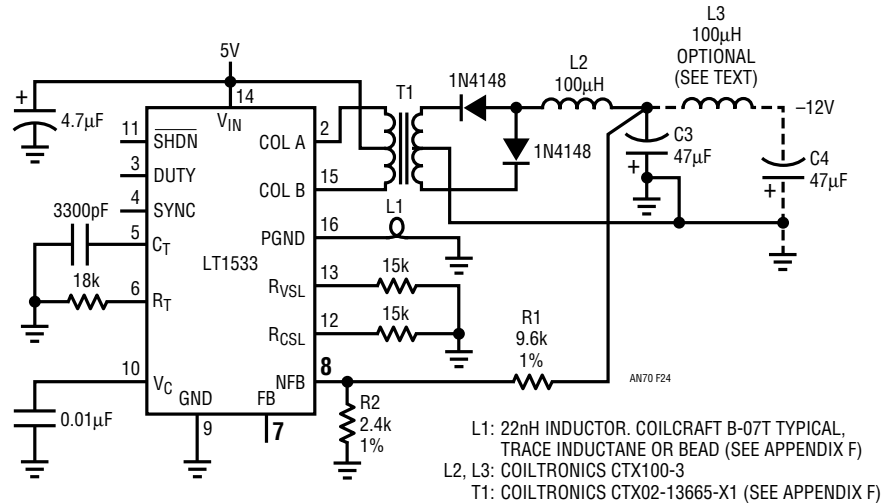


Figure 24. A Negative Output Version of Figure 5. LT1533's Negative Feedback Input Requires Minimal Configuration Changes. Noise Performance is Identical to Positive Output Version

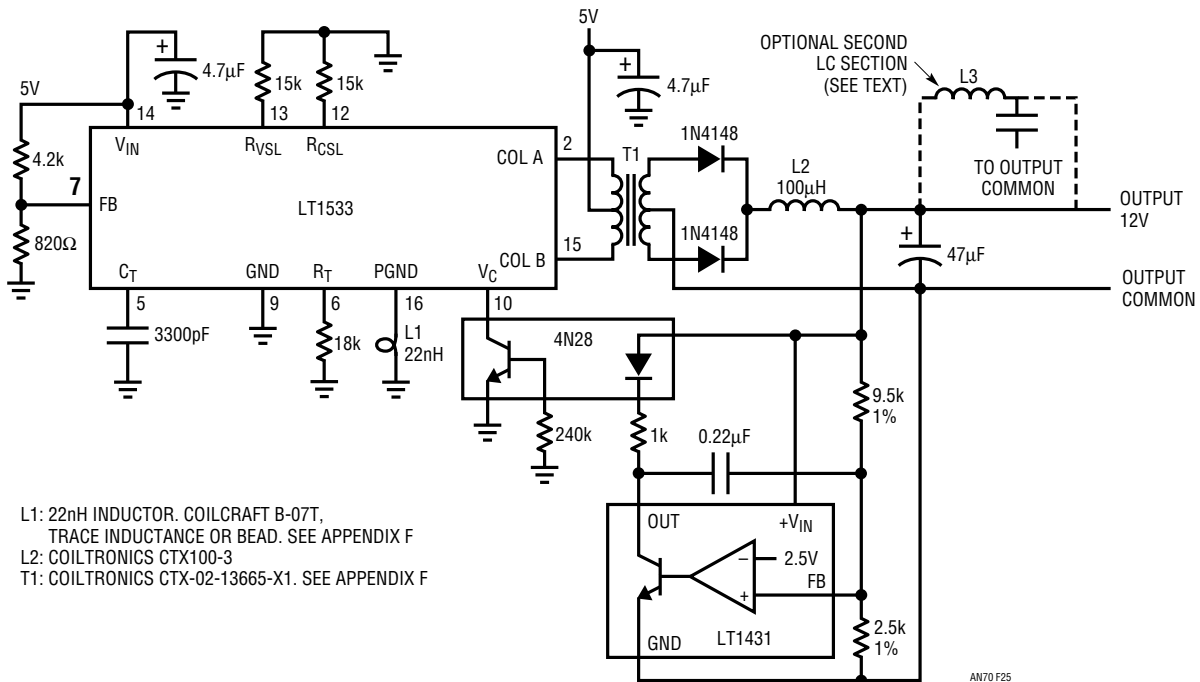


Figure 25. An Optoisolated Output Variant of Figure 5. Loop Closure to V_C Pin Bypasses LT1533 Error Amplifier, Enhancing Loop Stability. Noise Performance is Maintained

SPECIFICATION NOTICE

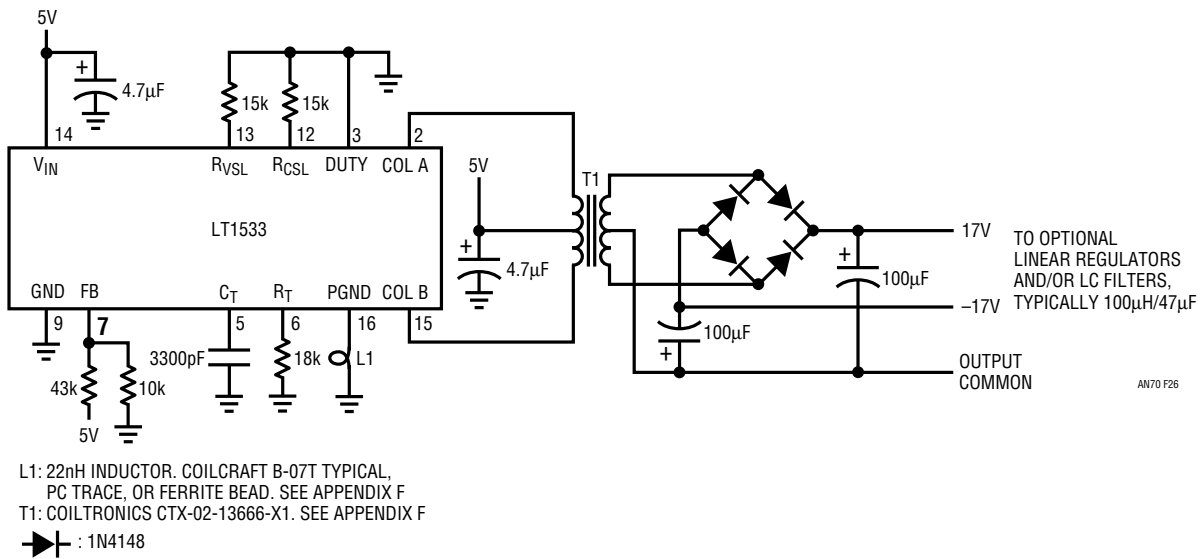


Figure 26. A Bipolar, Floating Output Converter. Grounding "DUTY" Pin and Biasing FB Puts Regulator into 50% Duty Cycle Mode. Floating, Unregulated Outputs Proportion to T1's Center Tap Voltage. Linear Regulators Are Optional

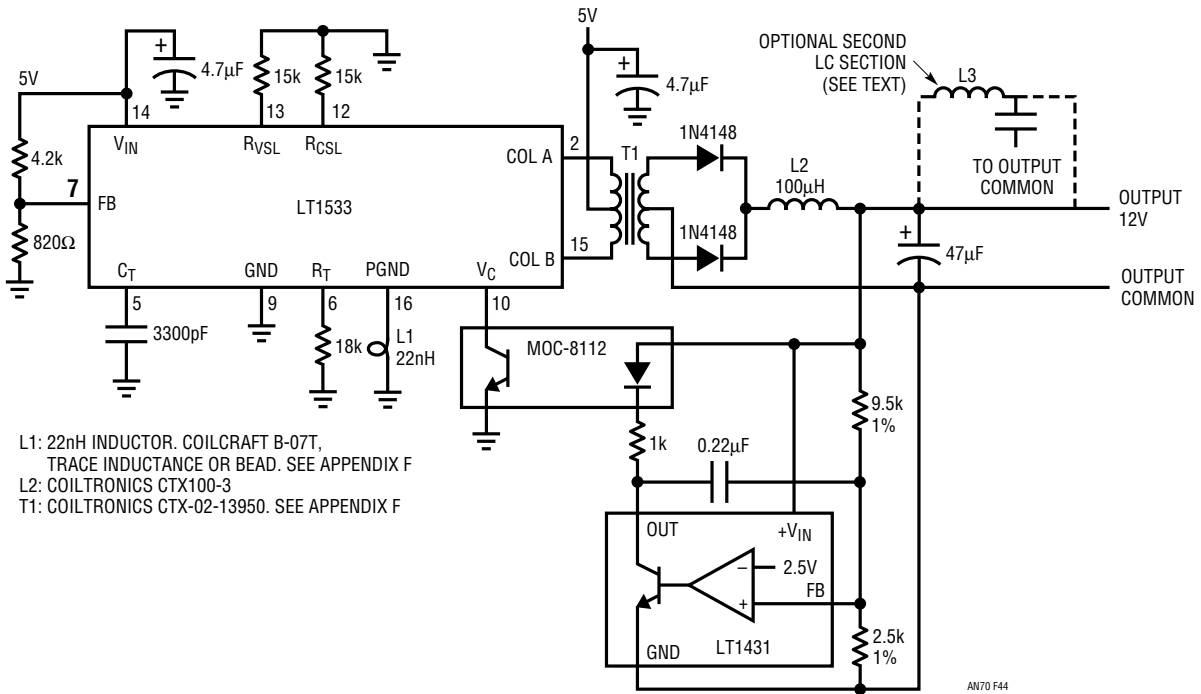


Figure 44. A 7500V Isolation Version of Figure 25. Transformer and Optoisolator Are Changed to Achieve Isolation and Noise Immunity. Circuit Operation is as Before

For further information regarding this specification notice contact:

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